

If there are questions regarding this Preliminary Amendment, please telephone  
Applicant's undersigned attorney.

Respectfully submitted,  
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**xNDD**

**MARKED-UP CLAIMS**

2. (Amended) The integrated circuit test system of claim 1, wherein elements of a first type condition [the] a clock signal provided to at least one flip-flop.

5. (Amended) The integrated circuit test system of [any of] claim 1, wherein elements of a third type include locking elements capable of preventing the propagation of at least one signal into the logic block.